



04-06-05

IFW

UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Gordon Ma et al.

Serial No.: 10/788,815

Filing Date: February 27, 2004

Title: LDMOS Transistor

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Group Art Unit: 2811

Examiner: Nguyen, Cuong Quang

Attny. Docket No. 068736.0230

Client Ref.: 2003P52733US

Mail Stop Response
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING VIA EXPRESS MAIL

PURSUANT TO 37 C.F.R. § 1.10, I HEREBY CERTIFY THAT I HAVE INFORMATION AND A REASONABLE BASIS FOR BELIEF THAT THIS CORRESPONDENCE WILL BE DEPOSITED WITH THE U.S. POSTAL SERVICE AS EXPRESS MAIL POST OFFICE TO ADDRESSEE, ON THE DATE BELOW, AND IS ADDRESSED TO:

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DATE OF MAILING: APRIL 5, 2005

Sir:

RESPONSE TO OFFICE ACTION DATED March 21, 2005

In response to the Examiner's Office Action of March 21, 2005, Applicant submits the following

Remarks.

As the one-month shortened statutory period for reply is due April 21, 2005, this Response is considered timely filed.